

Substitute for form 1449A/PTO				<i>Complete if Known</i>	
				Application Number	Not Yet Assigned <i>10/695927</i>
				Filing Date	October 30, 2003
				First Named Inventor	Yukihisa KINUGASA
				An Unit	<del>Not Yet Assigned</del> <i>2816</i>
				Examiner Name	Not Yet Assigned <i>WELL</i>
Sheet	1	of	1	Attorney Docket Number	031794-5

U.S. PATENT DOCUMENTS					
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	U.S. Patent Document Number - Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS					
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Application of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
Kan		JP 11-88071 A	03/30/1999	T. Harada et al	
Kan		JP 2001-44770 A	02/16/2001	K. Komatsu et al	

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS					
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
Kan		"Neuron-MOS Current Mirror Circuit and Its Application to Multi-Valued Logic" Jing SHEN et al. IEICE TRANS. INF. & SYST., VOL. E82-D, No. 5 May 1999 (pp 940-948)			

Examiner Signature	<i>WELLS</i>	Date Considered	<i>6/10/04</i>
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